ASSIGNMENT-5

1. Draw the signal block diagram of 80486 showing the signals associated with Address bus, Bus cycle definition, bus arbitration and bus size control. Discuss them in detail.
2. Draw the signal block diagram of 80486 showing the signals associated with Parity, Bus control, Bus arbitration, Interrupts and data bus. Discuss them in detail.
3. Explain the following features of Pentium processor:

(i) Advanced programmable Interrupt Controller(APIC)

(ii) System management mode (SMM)

1. Explain the following addressing modes of 68000 processor with an example for each

(i) Absolute addressing mode

(ii) Address Register Indirect Addressing mode

1. With a neat diagram of Programmer’s model of 68K explain the role of program counter and system byte of status register.